

# Embedded Systems

*Index covering the slides of the lecture from spring semester 2010 by Prof. Thiele, ETH Zurich.*

## A

activity diagram, 12-41  
allocation, 11-9  
aperiodic task, 4-46  
arrival time, 3-5  
As Late As Possible algorithm (ALAP), 11-20  
As Soon As Possible algorithm (ASAP), 11-17  
ASIC, 7-36  
average response time, 3-13

## B

background scheduling, 4-47  
binding, 11-9  
block diagrams, 10-25  
Bluetooth, 8-19

## C

characteristics of ES, 1-21  
class diagram, 12-45  
classical automata, 12-12  
co-operative multitasking, 2-41  
communication mechanisms, 6-19  
component diagram, 12-46  
computation time, 3-5  
context switching, 6-13  
control-data flow graph (CDFG), 10-8  
cost function, 11-5  
critical instant, 4-24  
critical section, 5-3  
CSMA  
    CD, 8-8  
    CA, 8-11  
    CR, 8-12

## D

data flow graph (DFG), 10-10  
deadline, 3-5  
Deadline Monotonic Scheduling (DM), 4-33  
dependence graph (DG), 10-4  
deployment diagram, 12-43  
digital signal processors (DSP), 7-22  
dynamic algorithms, 3-11

Dynamic Power Management (DPM), 9-45  
Dynamic Voltage Scaling (DVS), 9-12, 9-25, 11-55

## E

Earliest Deadline Due (EDD), 4-4  
Earliest Deadline First (EDF), 4-7  
    periodic, 4-40, 9-42  
Earliest Deadline First (EDF\*), 4-12  
exceeding time, 3-6  
exclusive resource, 5-5

## F

finishing time, 3-5  
finite state machine (FSM), 12-12  
FlexRay, 8-14  
FPGA, 7-30

## G

generic TT, 2-29

## I

indexed equation, 11-45  
Integer Linear Programming, 11-36, 11-51  
iterative algorithms, 11-44

## K

Kahn Process Networks, 12-36

## L

late tasks, 3-13  
latency, 11-10  
lateness, 3-6  
laxity, 3-6  
list scheduling, 11-31

## M

marked graph (MG), 10-18  
maximum lateness, 3-13  
Message Sequence Charts (MSC), 12-31  
microcontroller, 7-18  
mixed task sets, 4-46  
models of computation, 12-8

## **N**

non-preemptive algorithms, 3-11  
non-Preemptive ET, 2-33

## **P**

Parallelism, 9-17  
periodic task, 3-7, 4-18, 4-46  
petri nets, 2-43  
Pipelining, 9-18  
polar graph, 10-10  
Polling Server (RM), 4-49  
Power Supply Gating, 9-15  
precedence constraints, 3-9  
preemptive algorithms, 3-11  
preemptive ET, 2-35  
preemptive multitasking, 2-56  
Priority Inheritance Protocol (PIP), 5-10  
priority inversion, 5-6  
Process Networks, 12-35  
process states, 6-14  
processor utilization factor, 4-28, 4-41  
procrastination schedule, 9-49

## **R**

random access, 8-6  
Rate Monotonic Scheduling (RM), 4-22  
Real-Time operating system (RTOS), 6-9  
real-time systems, 2-10, 3-3  
release time, 3-5  
Resource Access Protocols, 5-9  
resource graph, 11-5

## **S**

Schedule, 3-4, 11-10  
sequence diagram, 12-42  
sequence graph, 11-5  
sequence graph (SG), 10-11  
simple periodic TT, 2-23  
single assignment form, 10-6  
slack time, 3-6  
specification techniques, 12-3  
sporadic task, 4-46  
stack policy, 2-35  
start time, 3-5  
StateCharts, 12-13

static algorithms, 3-11  
system specialization, 7-9  
System-on-Chip (SoC), 7-20, 7-38

## **T**

tardiness, 3-6  
task graph, 10-4  
TDMA, 8-7  
thread, 6-16  
timers, 12-22  
timing constraints, 11-23  
Token Ring, 8-10  
Total Bandwidth Server (EDF periodic), 4-54  
total completion time, 3-13  
TT Cyclic Executive, 2-25

## **U**

Unified Modeling Language (UML), 12-39  
use case diagram, 12-44

## **V**

very long instruction word (VLIW), 7-25, 9-21

## **W**

weighted constraint graph, 11-25

## **Y**

YDS algorithm, 9-35